

FIGURE 1a

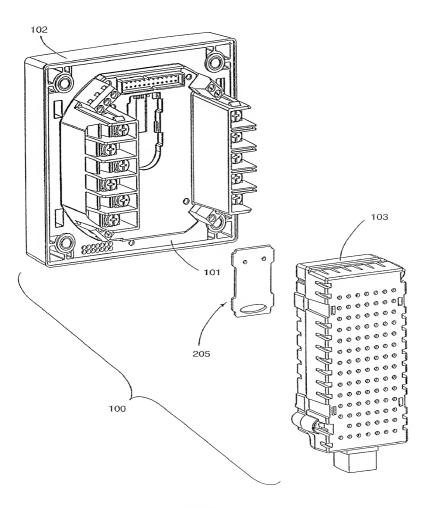


FIGURE 1b

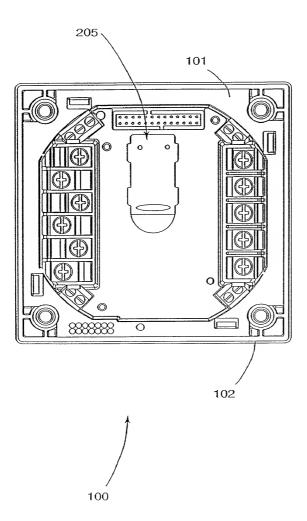


FIGURE 2

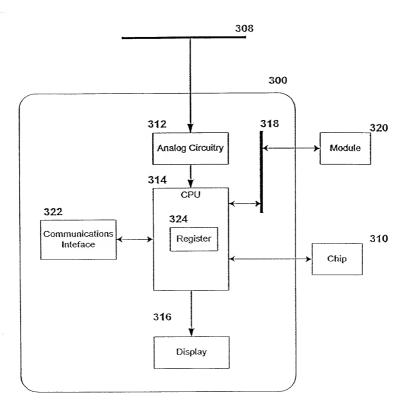


FIGURE 3a

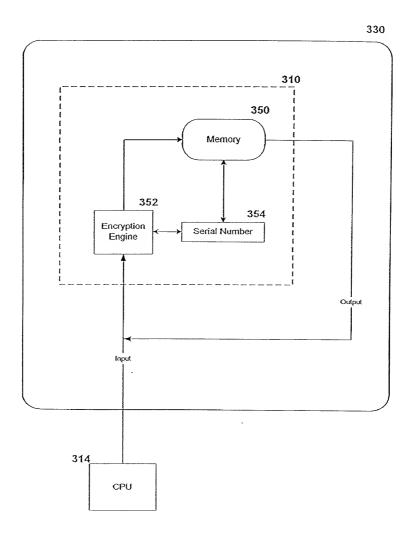


FIGURE 3b

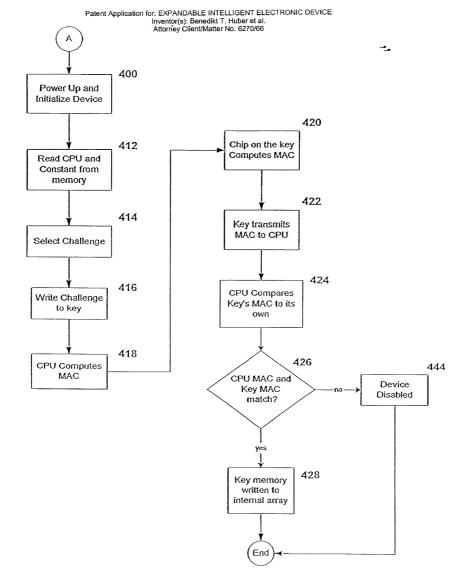
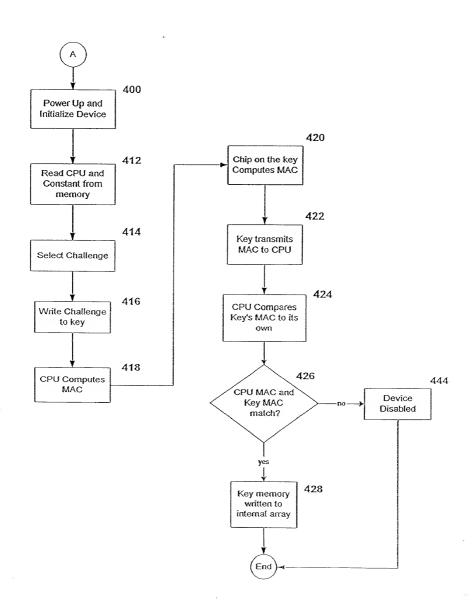


FIGURE 4



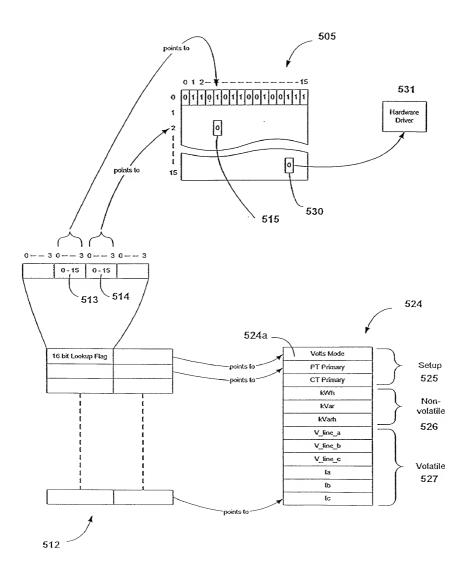


Figure 5

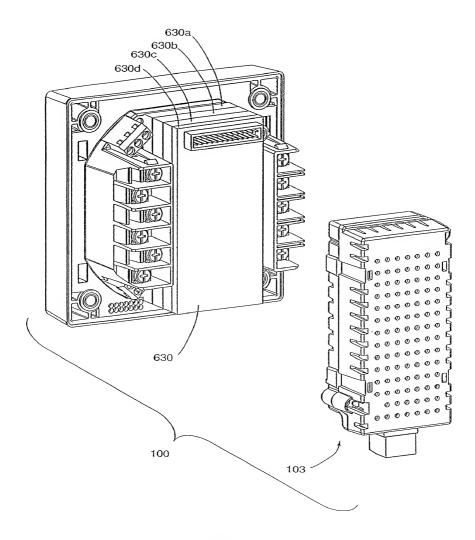
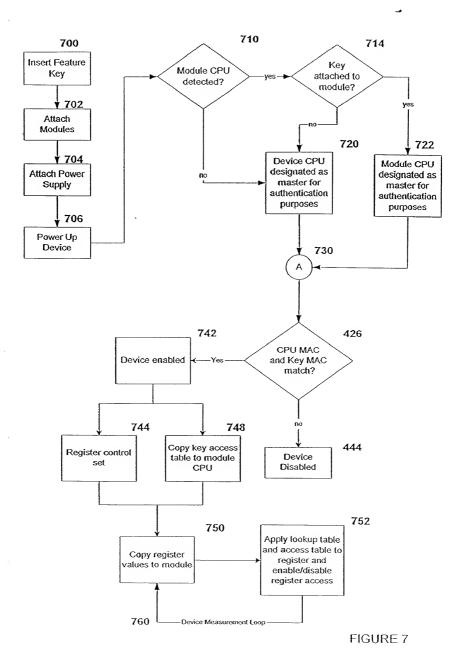


FIGURE 6



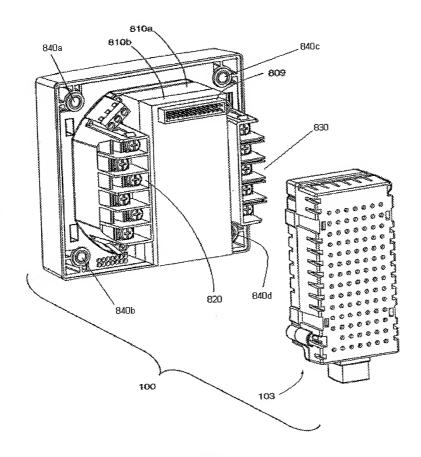


FIGURE 8

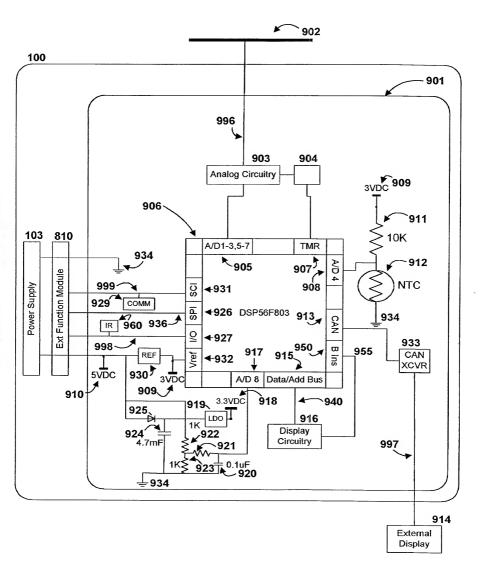


FIGURE 9

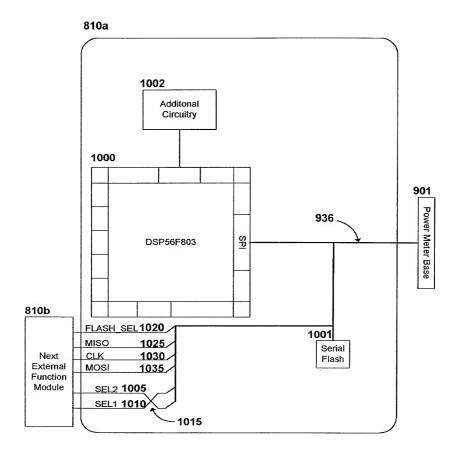
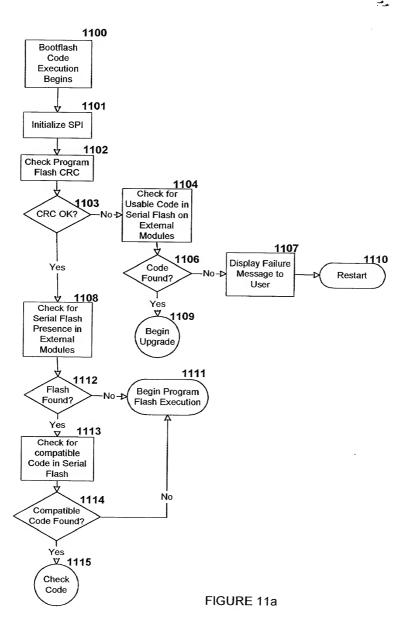


FIGURE 10



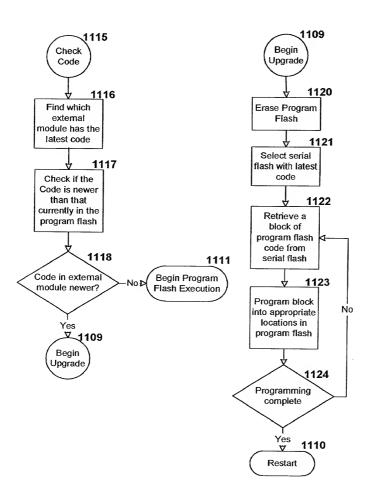


FIGURE 11b

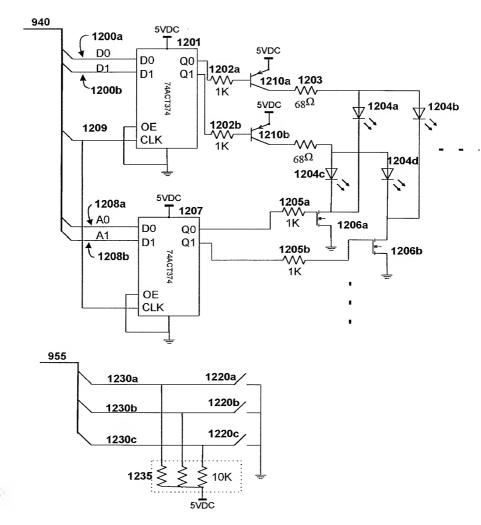


FIGURE 12

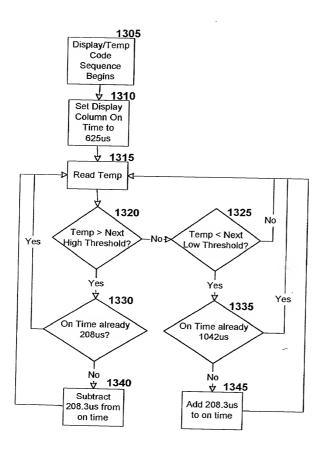


FIGURE 13

5	23	Ξ	2	<u>ო</u>	12   13   S#/C   D1   D2   D3   D4   D5   D6   D7   D8	2	D2	D3	7		90	20	8	2
2,7	-	90		1	:1410	1		4		. 1415 .				

Transmit Packet (From Master)

E2 E3 E4 E5 E6 E7	E3 E4 E5 E6	E2 E3 E4 E5 E6	E2 E3 E4 E5 E6	E2 E3 E4 E5 E6	NU NU E1 E2 E3 E4 E5 E6	NU NU E1 E2 E3 E4 E5 E6	NU NU E1 E2 E3 E4 E5 E6
E3 E4	E2 E3 E4	E2 E3 E4	E2 E3 E4	NU NU E1 E2 E3 E4	NU NU E1 E2 E3 E4	NU NU E1 E2 E3 E4	NU NU E1 E2 E3 E4
E2 E3	E2	E2	E2	NU NU E1 E2	NU NU E1 E2	NU NU E1 E2	NU NU E1 E2
E2				NU NU E1	NU NU E1	NU NU E1	NU NU E1
	딥	NU E1	NU NU E1	NU NU	NU NU	NU NU	NU NU

Receive Packet (From Slave)

FIGURE 1.

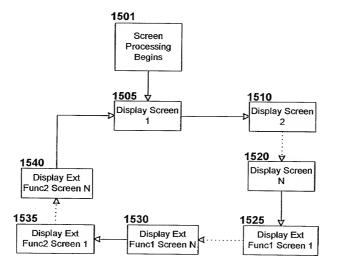


FIGURE 15

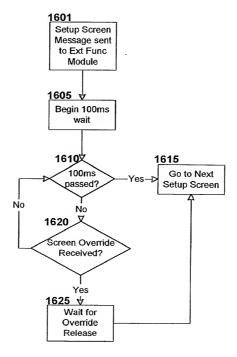
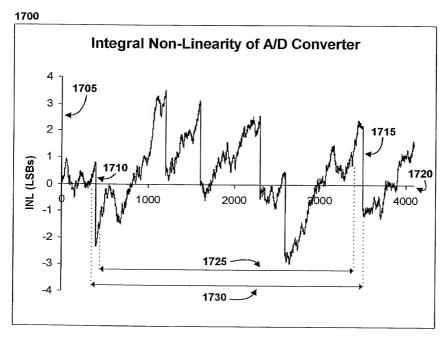


FIGURE 16



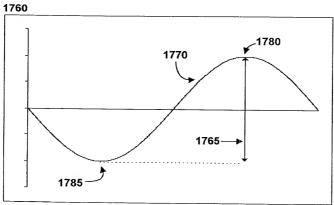
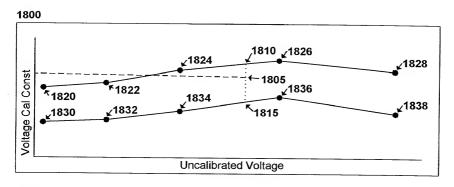
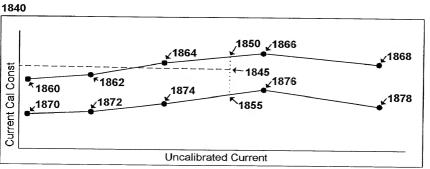


FIGURE 17





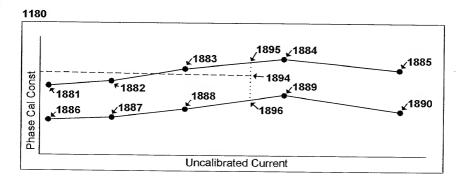
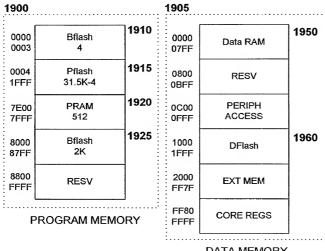


FIGURE 18

## Patent Application for: EXPANDABLE INTELLIGENT ELECTRONIC DEVICE Inventor(s): Benedikt T. Huber et al. Attorney Client/Matter No. 6270/66

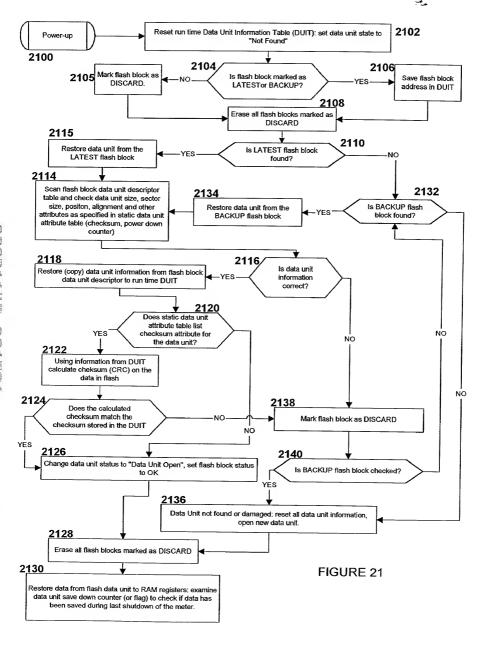


**DATA MEMORY** 

FIGURE 19

2000		:	2050					
	Page Flags	2005	Data Buffer Addr in RAM	2055				
	Data Unit Num	2010	Data Buffer Size in RAM	2060				
	Data Unit Size (Words)	2015	Checksum Enable Flag	2065				
2002	Data Unit Size (Sectors)	2020	Save Counter Enable Flag	2070				
	Data Unit Checksum	2025	Data Unit Sector Size	2075				
	Save Counter	2030	Number of Sectors per Flash Block	2080				
i i	Old Copy Desig	2035	:	2085				
	Data	2040	: Checksum Offset	2085				
:		]==	Save Counter Offset	2090				
Data	Data Unit Info in Data Flash Data Unit Info in Program Flash							

FIGURE 20



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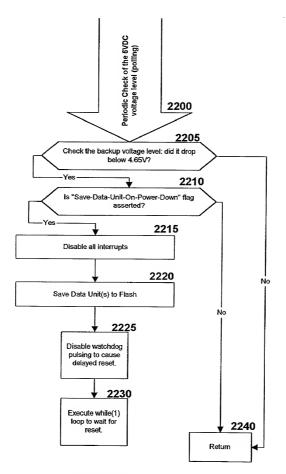


FIGURE 22

